

DATA-BASED CONTROL OF INTEGRATED CIRCUITS

Abstract of Disclosure

Integrated circuit chips can have a programmable data reference table that provides information required for circuit blocks on the chip to attain a desired performance, such as a certain power consumption and/or clock speed. The information entered into this table is based on data obtained from actual tests performed on the chip either when it is on a wafer or after it has been cut from the wafer. The tests determine the clock rates, supply voltages, and back-bias voltages at which the chip can successfully execute a program.

Figures

Figure 1: A schematic diagram of a neural network architecture. The diagram shows a sequence of layers: an input layer, followed by a series of hidden layers, and an output layer. The layers are connected by arrows indicating the flow of information. The diagram is labeled with 'Input', 'Hidden', and 'Output' layers. The diagram is also labeled with 'Figure 1'.